

**CoolMOS™ Power Transistor**
**Features**

- Lowest figure-of-merit  $R_{ON} \times Q_g$
- Ultra low gate charge
- Extreme dv/dt rated
- High peak current capability
- Qualified according to JEDEC<sup>(1)</sup> for target applications
- Pb-free lead plating; RoHS compliant

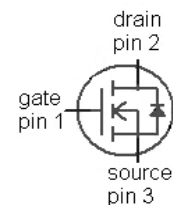
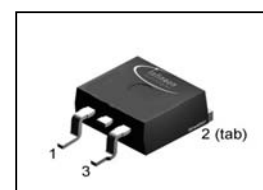
**CoolMOS CP is specially designed for:**

- Hard switching topologies, for Server and Telecom

**Product Summary**

$V_{DS} @ T_{j,max}$	650	V
$R_{DS(on),max}$	0.125	$\Omega$
$Q_{g,typ}$	53	nC

PG-TO263



Type	Package	Ordering Code	Marking
IPP60R125CP	PG-TO220	SP000088488	6R125P

**Maximum ratings, at  $T_j=25\text{ }^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	$I_D$	$T_C=25\text{ }^\circ\text{C}$	25	A
		$T_C=100\text{ }^\circ\text{C}$	16	
Pulsed drain current <sup>(2)</sup>	$I_{D,pulse}$	$T_C=25\text{ }^\circ\text{C}$	82	
Avalanche energy, single pulse	$E_{AS}$	$I_D=11\text{ A}$ , $V_{DD}=50\text{ V}$	708	mJ
Avalanche energy, repetitive $t_{AR}$ <sup>(2),3)</sup>	$E_{AR}$	$I_D=11\text{ A}$ , $V_{DD}=50\text{ V}$	1.2	
Avalanche current, repetitive $t_{AR}$ <sup>(2),3)</sup>	$I_{AR}$		11	A
MOSFET dv/dt ruggedness	dv/dt	$V_{DS}=0\dots480\text{ V}$	50	V/ns
Gate source voltage	$V_{GS}$	static	$\pm 20$	V
		AC ( $f>1\text{ Hz}$ )	$\pm 30$	
Power dissipation	$P_{tot}$	$T_C=25\text{ }^\circ\text{C}$	208	W
Operating and storage temperature	$T_j, T_{stg}$		-55 ... 150	$^\circ\text{C}$

**Maximum ratings, at  $T_j=25\text{ °C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value	Unit
Continuous diode forward current	$I_S$	$T_C=25\text{ °C}$	16	A
Diode pulse current <sup>2)</sup>	$I_{S,pulse}$		82	
Reverse diode $dv/dt$ <sup>4)</sup>	$dv/dt$		15	V/ns

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

**Thermal characteristics**

Thermal resistance, junction - case	$R_{thJC}$		-	-	0.6	K/W
Thermal resistance, junction - ambient	$R_{thJA}$	SMD version, device on PCB, minimal footprint	-	-	62	
		SMD version, device on PCB, 6 cm <sup>2</sup> cooling area <sup>5)</sup>	-	35	-	
Soldering temperature, wave- & reflowsoldering allowed	$T_{sold}$	reflow MSL1	-	-	260	°C

**Electrical characteristics, at  $T_j=25\text{ °C}$ , unless otherwise specified**
**Static characteristics**

Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS}=0\text{ V}$ , $I_D=250\text{ }\mu\text{A}$	600	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_D=1.1\text{ mA}$	2.5	3	3.5	
Zero gate voltage drain current	$I_{DSS}$	$V_{DS}=600\text{ V}$ , $V_{GS}=0\text{ V}$ , $T_j=25\text{ °C}$	-	-	2	$\mu\text{A}$
		$V_{DS}=600\text{ V}$ , $V_{GS}=0\text{ V}$ , $T_j=150\text{ °C}$	-	20	-	
Gate-source leakage current	$I_{GSS}$	$V_{GS}=20\text{ V}$ , $V_{DS}=0\text{ V}$	-	-	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$V_{GS}=10\text{ V}$ , $I_D=16\text{ A}$ , $T_j=25\text{ °C}$	-	0.11	0.125	$\Omega$
		$V_{GS}=10\text{ V}$ , $I_D=16\text{ A}$ , $T_j=150\text{ °C}$	-	0.30	-	
Gate resistance	$R_G$	$f=1\text{ MHz}$ , open drain	-	2.1	-	$\Omega$

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

**Dynamic characteristics**

Input capacitance	$C_{iss}$	$V_{GS}=0\text{ V}, V_{DS}=100\text{ V},$ $f=1\text{ MHz}$	-	2500	-	pF
Output capacitance	$C_{oss}$		-	120	-	
Effective output capacitance, energy related <sup>6)</sup>	$C_{o(er)}$	$V_{GS}=0\text{ V}, V_{DS}=0\text{ V}$ to 480 V	-	110	-	
Effective output capacitance, time related <sup>7)</sup>	$C_{o(tr)}$		-	300	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=400\text{ V},$ $V_{GS}=10\text{ V}, I_D=16\text{ A},$ $R_G=3.3\ \Omega$	-	15	-	ns
Rise time	$t_r$		-	5	-	
Turn-off delay time	$t_{d(off)}$		-	50	-	
Fall time	$t_f$		-	5	-	

**Gate Charge Characteristics**

Gate to source charge	$Q_{gs}$	$V_{DD}=400\text{ V}, I_D=16\text{ A},$ $V_{GS}=0\text{ to }10\text{ V}$	-	12	-	nC
Gate to drain charge	$Q_{gd}$		-	18	-	
Gate charge total	$Q_g$		-	53	70	
Gate plateau voltage	$V_{plateau}$		-	5.0	-	V

**Reverse Diode**

Diode forward voltage	$V_{SD}$	$V_{GS}=0\text{ V}, I_F=16\text{ A},$ $T_j=25\text{ }^\circ\text{C}$	-	0.9	1.2	V
Reverse recovery time	$t_{rr}$	$V_R=400\text{ V}, I_F=I_S,$ $di_F/dt=100\text{ A}/\mu\text{s}$	-	430	-	ns
Reverse recovery charge	$Q_{rr}$		-	9	-	$\mu\text{C}$
Peak reverse recovery current	$I_{rrm}$		-	42	-	A

<sup>1)</sup> J-STD20 and JESD22

<sup>2)</sup> Pulse width  $t_p$  limited by  $T_{j,max}$

<sup>3)</sup> Repetitive avalanche causes additional power losses that can be calculated as  $P_{AV}=E_{AR} \cdot f$ .

<sup>4)</sup>  $I_{SD}=I_D, di/dt \leq 200\text{ A}/\mu\text{s}, V_{DClk}=400\text{ V}, V_{peak} < V_{(BR)DSS}, T_j < T_{j,max}$ , identical low side and high side switch.

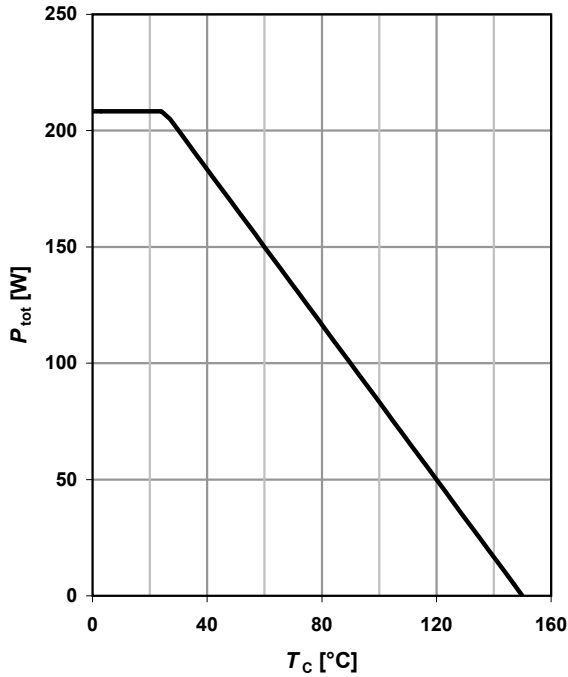
<sup>5)</sup> Device on 40mm\*40mm\*1.5 epoxy PCB FR4 with 6cm<sup>2</sup> (one layer, 70 $\mu\text{m}$  thick) copper area for drain connection. PCB is vertical without blown air

<sup>6)</sup>  $C_{o(er)}$  is a fixed capacitance that gives the same stored energy as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>7)</sup>  $C_{o(tr)}$  is a fixed capacitance that gives the same charging time as  $C_{oss}$  while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

**1 Power dissipation**

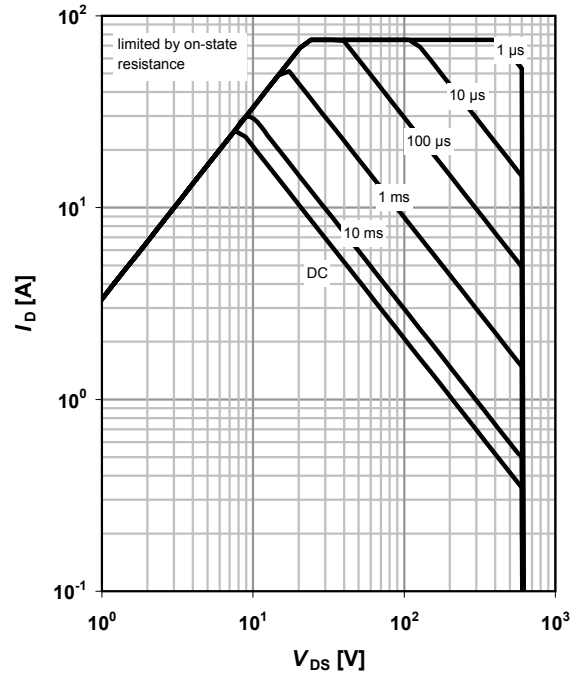
$P_{tot}=f(T_C)$



**2 Safe operating area**

$I_D=f(V_{DS}); T_C=25\text{ °C}; D=0$

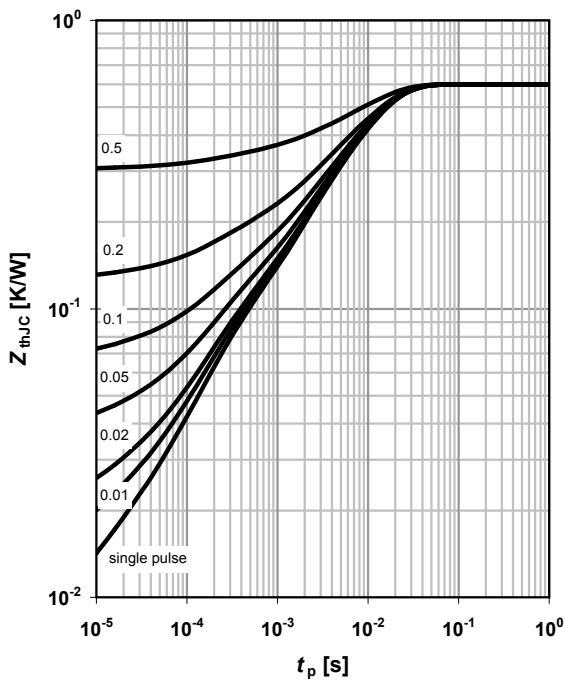
parameter:  $t_p$



**3 Max. transient thermal impedance**

$Z_{thJC}=f(t_p)$

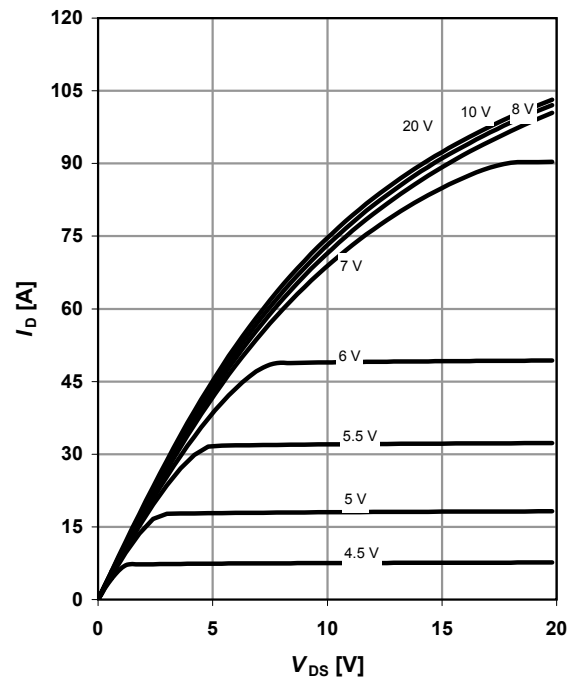
parameter:  $D=t_p/T$



**4 Typ. output characteristics**

$I_D=f(V_{DS}); T_j=25\text{ °C}$

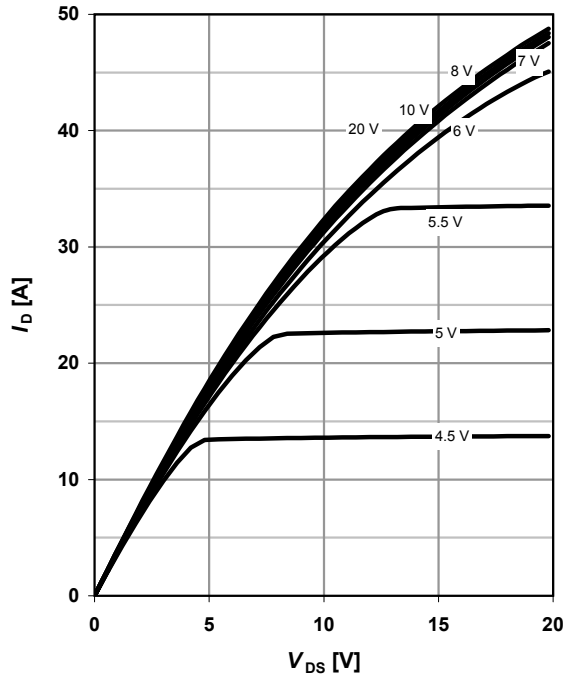
parameter:  $V_{GS}$



**5 Typ. output characteristics**

$I_D = f(V_{DS}); T_j = 150\text{ }^\circ\text{C}$

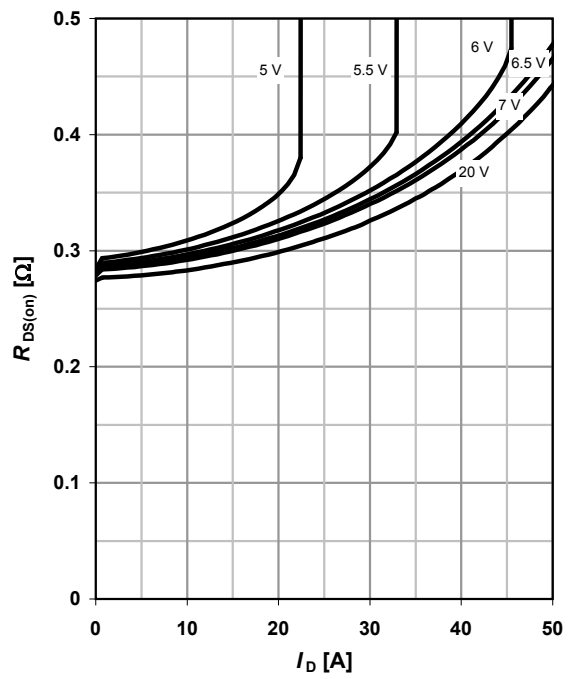
parameter:  $V_{GS}$



**6 Typ. drain-source on-state resistance**

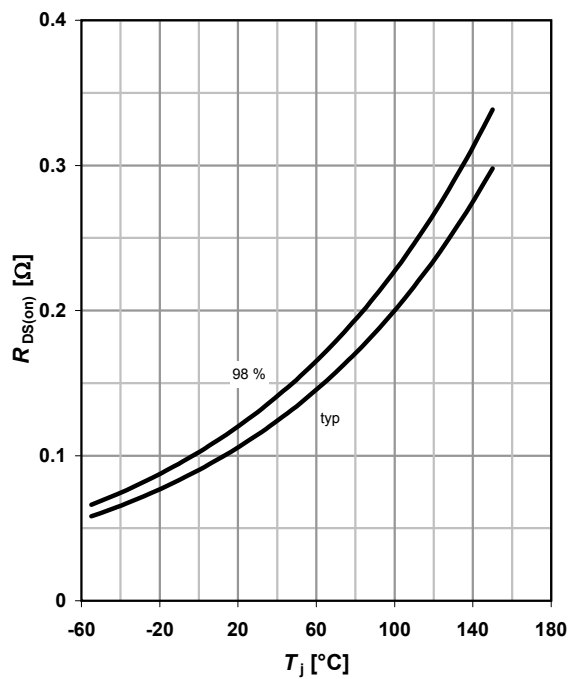
$R_{DS(on)} = f(I_D); T_j = 150\text{ }^\circ\text{C}$

parameter:  $V_{GS}$



**7 Drain-source on-state resistance**

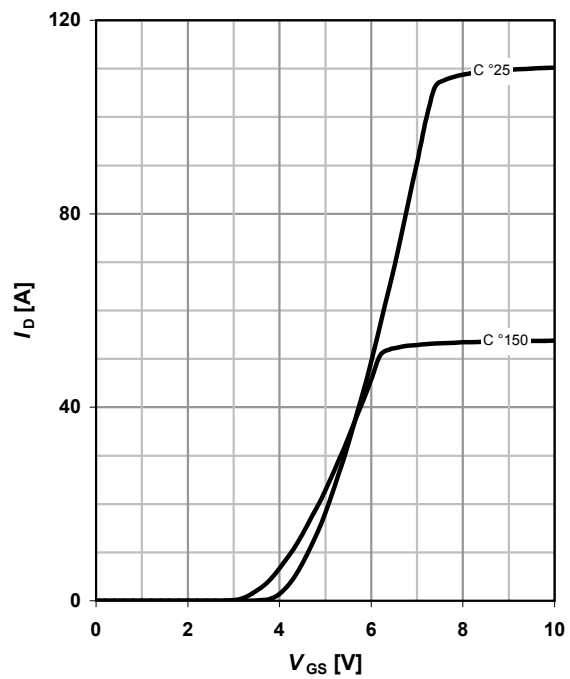
$R_{DS(on)} = f(T_j); I_D = 16\text{ A}; V_{GS} = 10\text{ V}$



**8 Typ. transfer characteristics**

$I_D = f(V_{GS}); |V_{DS}| > 2|I_D|R_{DS(on)max}$

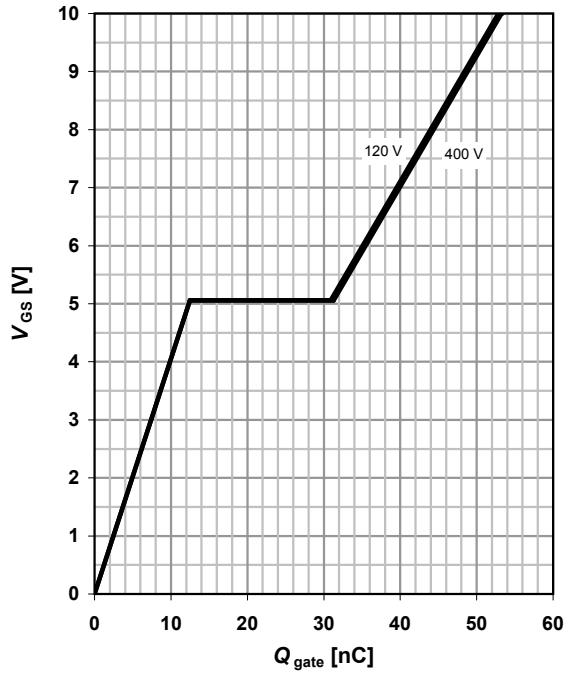
parameter:  $T_j$



**9 Typ. gate charge**

$V_{GS}=f(Q_{gate}); I_D=16\text{ A pulsed}$

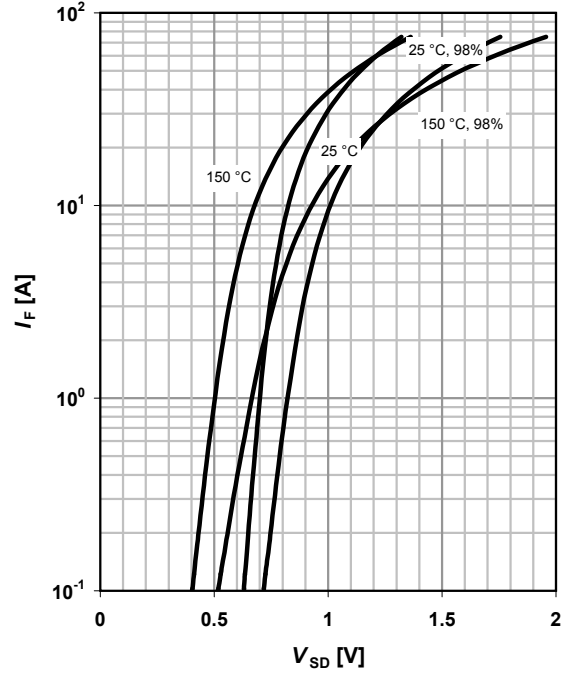
parameter:  $V_{DD}$



**10 Forward characteristics of reverse diode**

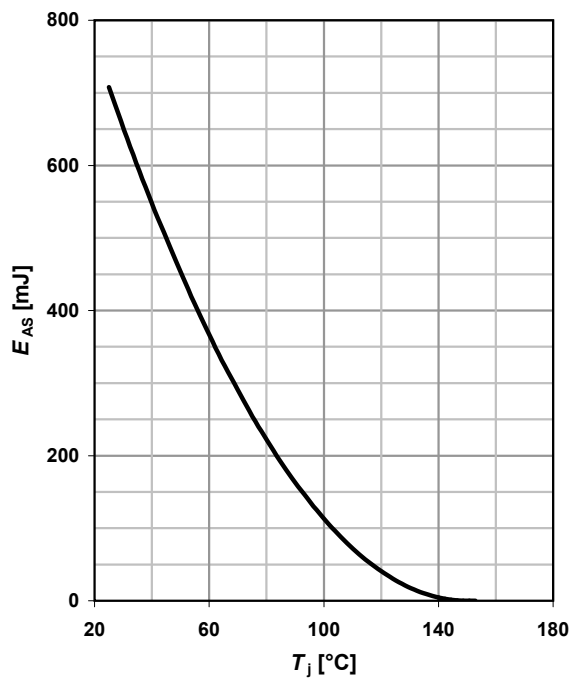
$I_F=f(V_{SD})$

parameter:  $T_j$



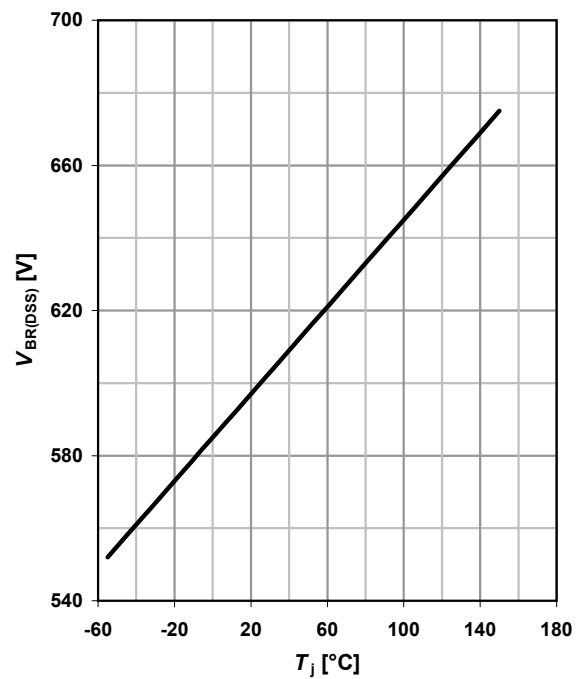
**11 Avalanche energy**

$E_{AS}=f(T_j); I_D=11\text{ A}; V_{DD}=50\text{ V}$



**12 Drain-source breakdown voltage**

$V_{BR(DSS)}=f(T_j); I_D=0.25\text{ mA}$

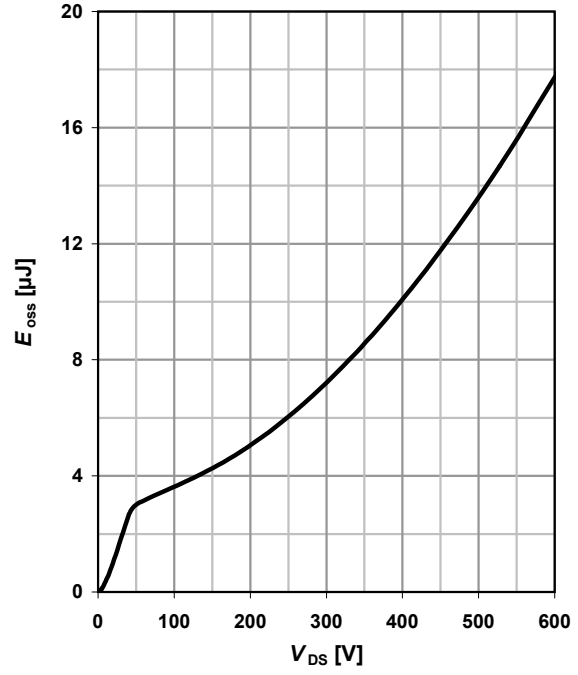
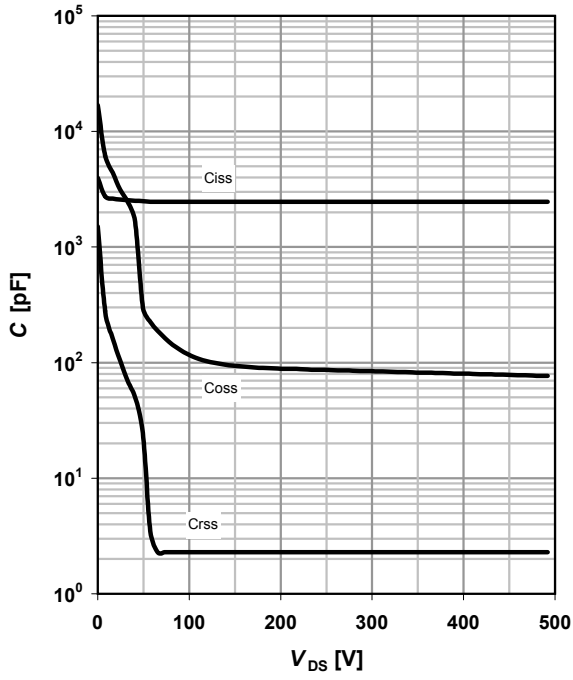


13 Typ. capacitances

$C=f(V_{DS}); V_{GS}=0\text{ V}; f=1\text{ MHz}$

14 Typ. Coss stored energy

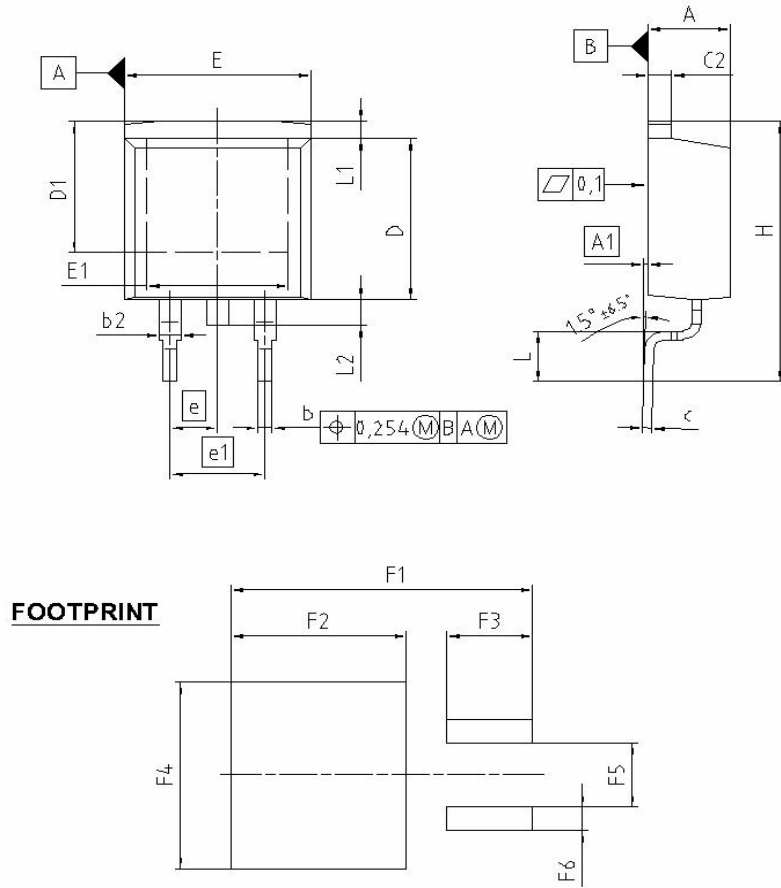
$E_{oss}=f(V_{DS})$



Definition of diode switching characteristics





**PG-TO263-3-2/TO263-3-5/TO263-3-22: Outlines**


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
<b>A</b>	4.300	4.572	0.169	0.180
<b>A1</b>	0.000	0.254	0.000	0.010
<b>b</b>	0.850	0.850	0.026	0.033
<b>b2</b>	0.950	1.321	0.037	0.052
<b>c</b>	0.330	0.650	0.013	0.026
<b>c2</b>	0.170	1.400	0.046	0.055
<b>D</b>	8.509	9.450	0.335	0.372
<b>D1</b>	7.100	-	0.280	-
<b>E</b>	9.800	10.312	0.386	0.406
<b>E1</b>	6.500	-	0.256	-
<b>e</b>	2.540		0.100	
<b>e1</b>	5.080		0.200	
<b>N</b>	2		2	
<b>H</b>	14.605	15.875	0.575	0.625
<b>L</b>	2.200	3.000	0.087	0.118
<b>L1</b>	-	1.600	-	0.063
<b>L2</b>	1.000	1.778	0.039	0.070
<b>F1</b>	16.050	16.250	0.632	0.640
<b>F2</b>	9.300	9.500	0.366	0.374
<b>F3</b>	4.500	4.700	0.177	0.185
<b>F4</b>	10.700	10.900	0.421	0.429
<b>F5</b>	3.630	3.830	0.143	0.151
<b>F6</b>	1.100	1.300	0.043	0.051

<b>REFERENCE</b> JEDEC TO263
<b>SCALE</b> 0 5 5 7.5mm
<b>EUROPEAN PROJECTION</b> 
<b>ISSUE DATE</b> 12-02-2006
<b>FILE</b> TO263_2

Dimensions in mm/inches:

**Published by**  
**Infineon Technologies AG**  
**81726 München, Germany**  
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