Power MOSFET 6.0 Amps, 20 Volts

N-Channel Enhancement Mode Dual SO-8 Package

Features

- Ultra Low R_{DS(on)}
- Higher Efficiency Extending Battery Life
- Logic Level Gate Drive
- Miniature Dual SOIC-8 Surface Mount Package
- Diode Exhibits High Speed, Soft Recovery
- Avalanche Energy Specified
- SOIC-8 Mounting Information Provided
- Pb-Free Package is Available

Applications

- DC–DC Converters
- Low Voltage Motor Control
- Power Management in Portable and Battery-Powered Products, for example, Computers, Printers, Cellular and Cordless Telephones and PCMCIA Cards

MAXIMUM RATINGS (T₁ = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSS}	20	V
Drain-to-Gate Voltage (R_{GS} = 1.0 M Ω)	V _{DGR}	20	V
Gate-to-Source Voltage - Continuous	V _{GS}	±12	V
Thermal Resistance, Junction-to-Ambient (Note 1) Total Power Dissipation @ $T_A = 25^{\circ}C$ Continuous Drain Current @ $T_A = 25^{\circ}C$ Continuous Drain Current @ $T_A = 70^{\circ}C$ Pulsed Drain Current (Note 4)	R _{θJA} P _D I _D I _{DM}	62.5 2.0 6.5 5.5 50	°C/W W A A A
Thermal Resistance, Junction-to-Ambient (Note 2) Total Power Dissipation @ $T_A = 25^{\circ}C$ Continuous Drain Current @ $T_A = 25^{\circ}C$ Continuous Drain Current @ $T_A = 70^{\circ}C$ Pulsed Drain Current (Note 4)	R _{θJA} P _D I _D I _D	102 1.22 5.07 4.07 40	°C/W W A A A
Thermal Resistance Junction-to-Ambient (Note 3) Total Power Dissipation @ $T_A = 25^{\circ}C$ Continuous Drain Current @ $T_A = 25^{\circ}C$ Continuous Drain Current @ $T_A = 70^{\circ}C$ Pulsed Drain Current (Note 4)	R _{θJA} P _D I _D I _{DM}	172 0.73 3.92 3.14 30	°C/W W A A A

1. Mounted onto a 2 in square FR-4 Board

(1 in sq. 2 oz. Cu 0.06 in thick single sided), t < 10 seconds. 2. Mounted onto a 2 in square FR-4 Board

(1 in sq. 2 oz. Cu 0.06 in thick single sided), t = steady state.

3. Minimum FR-4 or G-10 PCB, t = steady state.

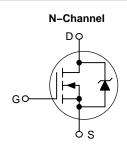
4. Pulse Test: Pulse Width = 10 μ s, Duty Cycle = 2%.



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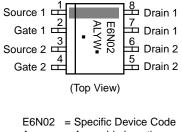
V _{DSS}	R _{DS(ON)} TYP	I _D MAX
20 V	$35 \text{ m}\Omega @ \text{V}_{\text{GS}} = 4.5 \text{ V}$	6.0 A





CASE 751 STYLE 11

MARKING DIAGRAM & PIN ASSIGNMENT



А	= Assembly Location
Y	= Year
WW	= Work Week
•	= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NTMD6N02R2	SOIC-8	2500/Tape & Reel
NTMD6N02R2G	SOIC-8 (Pb-Free)	2500/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MAXIMUM RATINGS (T₁ = 25° C unless otherwise noted) (continued)

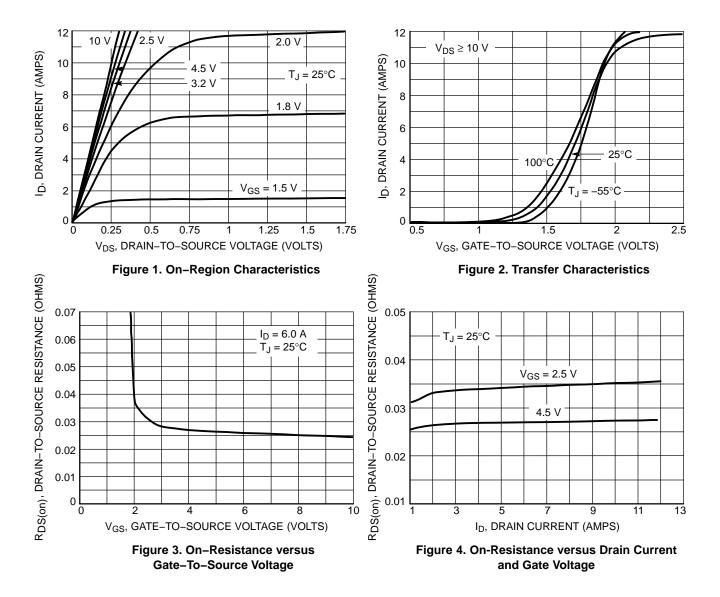
Rating		Symbol		Value		Unit
Operating and Storage Temperature Range		T _J , T _{stg}	-55 to +150		0	°C
Single Pulse Drain–to–Source Avalanche Energy – Starting T _J = 25°C (V_{DD} = 20 Vdc, V_{GS} = 5.0 Vdc, Peak I _L = 6.0 Apk, L = 20 mH, R _G = 25 Ω)		E _{AS}	360			mJ
Maximum Lead Temperature for Soldering Purposes for 1	0 seconds	ΤL		260		°C
LECTRICAL CHARACTERISTICS (T _C = 25°C unles	s otherwise noted) (Note	e 5)				
Characteristic		Symbol	Min	Тур	Max	Unit
FF CHARACTERISTICS						-
Drain-to-Source Breakdown Voltage ($V_{GS} = 0 Vdc, I_D = 250 \mu Adc$) Temperature Coefficient (Positive)		V _{(BR)DSS}	20 _	_ 19.2		Vdc mV/°C
Zero Gate Voltage Drain Current $(V_{DS} = 20 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 25^{\circ}\text{C})$ $(V_{DS} = 20 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$		I _{DSS}	-		1.0 10	μAdc
Gate-Body Leakage Current (V_{GS} = +12 Vdc, V_{DS} = 0 V	dc)	I _{GSS}	-	-	100	nAdc
Gate-Body Leakage Current (V_{GS} = -12 Vdc, V_{DS} = 0 V	dc)	I _{GSS}	-	-	-100	nAdc
N CHARACTERISTICS						
Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = -250 \ \mu Adc$) Temperature Coefficient (Negative)		V _{GS(th)}	0.6 -	0.9 -3.0	1.2 -	Vdc mV/°0
$ Static Drain-to-Source On-State Resistance \\ (V_{GS} = 4.5 Vdc, I_D = 6.0 Adc) \\ (V_{GS} = 4.5 Vdc, I_D = 4.0 Adc) \\ (V_{GS} = 2.7 Vdc, I_D = 2.0 Adc) \\ (V_{GS} = 2.5 Vdc, I_D = 3.0 Adc) \\ (V_{GS} = 2.5 Vdc, I_D = 3.0 Adc) $		R _{DS(on)}	- - -	0.028 0.028 0.033 0.035	0.035 0.043 0.048 0.049	Ω
Forward Transconductance (V _{DS} = 12 Vdc, I_D = 3.0 Adc)	1	9FS	-	10	-	Mhos
YNAMIC CHARACTERISTICS		•		•	•	-
Input Capacitance (V _{DS} = 16 Vdc	$V_{cc} = 0 V dc$	C _{iss}	-			

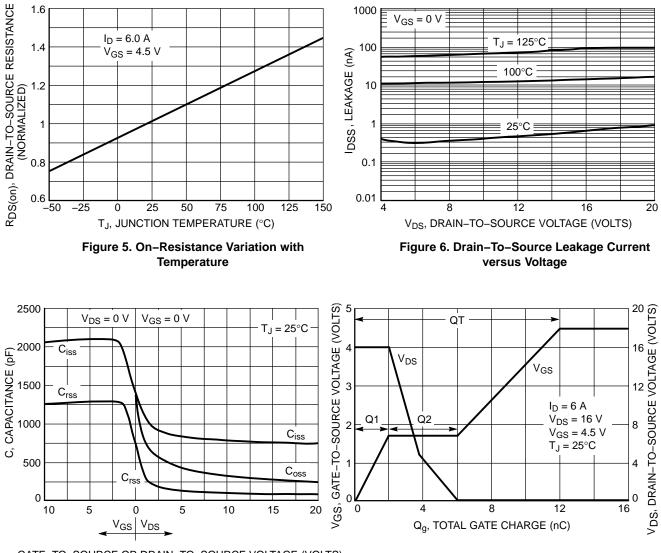
 $(V_{DS} = 16 \ \text{Vdc}, \ \text{V}_{GS} = 0 \ \text{Vdc}, \\ f = 1.0 \ \text{MHz})$

1

Characteristic		Symbol	Min	Тур	Max	Unit
BODY-DRAIN DIODE RATINGS (Note	9)					
Diode Forward On–Voltage		V _{SD}		0.83 0.88 0.75	1.1 1.2 -	Vdc
Reverse Recovery Time		t _{rr}	-	30	-	ns
	(I _S = 6.0 Adc, V _{GS} = 0 Vdc, dI _S /dt = 100 A/us)	ta	-	15	-	
		t _b	-	15	-	
Reverse Recovery Stored Charge		Q _{RR}	-	0.02	-	μC

8. Handling precautions to protect against electrostatic discharge is mandatory. 9. Indicates Pulse Test: Pulse Width = $300 \ \mu s \ max$, Duty Cycle = 2%.

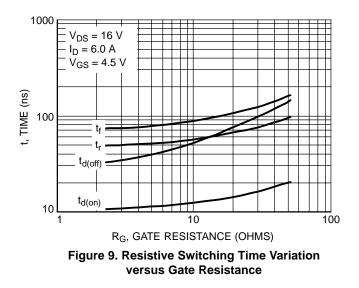




GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate–To–Source and Drain–To–Source Voltage versus Total Charge



DRAIN-TO-SOURCE DIODE CHARACTERISTICS

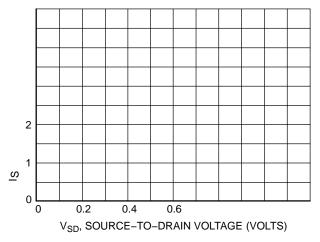
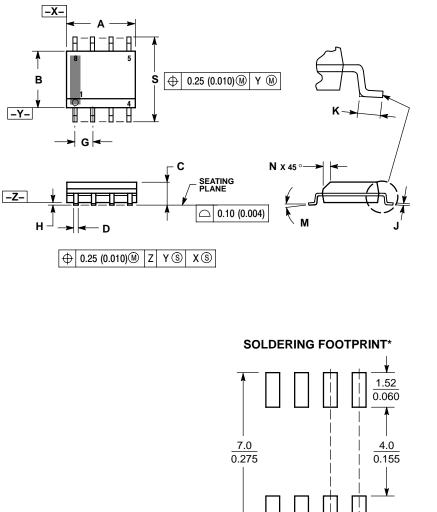


Figure 10. Diode Forward Voltage versus Current

PACKAGE DIMENSIONS

SOIC-8 CASE 751-07 ISSUE AG



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETER.
 DIMENSION A AND B DO NOT INCLUDE
- MOLD PROTRUSION. 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006)
- PER SIDE. 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT
- MAXIMUM MATERIAL CONDITION. 6. 751–01 THRU 751–06 ARE OBSOLETE. NEW STANDARD IS 751–07.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27	7 BSC	0.050 BSC	
Н	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
κ	0.40	1.27	0.016	0.050
М	0 °	8 °	0 °	8 °
Ν	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

STYLE 11:

LE 11:	
YIN 1.	SOURCE 1
2.	GATE 1
3.	SOURCE 2

4.	GATE 2	

5.	DRAIN	2
<u> </u>		0

υ.	DIVAN	2
7.	DRAIN	1

8.	DRAIN	1

details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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