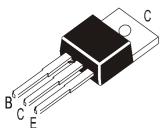




PLASTIC POWER TRANSISTORS



TIP41, A, B, C NPN TIP42, A, B, C PNP

TO-220 Plastic Package For Lead Free Parts, Device Part # will be Prefixed with "T"

Complementary Silicon Transistors intended for a wide variety of Switching and Amplifier Applications, Series and Shunt Regulators, Driver and Output stages of Hi-Fi Amplifiers

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

DESCRIPTION	SYMBOL	TIP41	TIP41A	TIP41B	TIP41C	
DESCRIPTION		TIP42	TIP42A	TIP42B	TIP42C	
Collector Emitter Voltage	V _{CEO}	40	60	80	100	V
Collector Base Voltage	V _{CBO}	40	60	80	100	V
Emitter Base Voltage	V _{EBO}		5	.0		V
Collector Current Continuous	I _C		6.0			
Collector Current Peak	I _{CM}	10				А
Base Current	I _B	2.0				Α
Power Dissipation upto T _c =25 ^o C	P _D	65				W
Derate above 25°C		520				mW/ºC
Power Dissipation upto T _a =25°C	P _D	2.0			W	
Derate above 25°C		16				mW/ºC
Unclamped Inductive Load Energy	*E	62.5			mJ	
Storage Temperature	T _{stg}	150			°C	
Junction Temperature	Tj	- 65 to +150			°C	

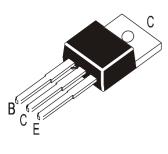
THERMAL RESISTANCE

Junction to Case	R _{th (j-c)}	1.92	°C/W
Junction to Ambient in free air	R _{th (j-a)}	62.5	°C/W

* I_c=2.5A, L=20mH, P.R.F.=10Hz, V_{cc} =10V, R_{BE} =100W

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ELECTRICAL CHARACTERISTICS (Tc=25°C unless specified otherwise)

SYMBOL	TEST CONDITION	EST CONDITION MIN		UNIT
*V _{CEO}	I _C =30mA, I _B =0			
	TIP41/42	40		V
	TIP41A/42A	60		V
	TIP41B/42B	80		V
	TIP41C/42C	100		V
I _{CEO}	V _{CE} =30V, I _B =0			
	TIP41, A / 42, A		0.7	mA
	V _{CE} =60V, I _B =0			
	TIP41B, C / 42B, C		0.7	mA
I _{CES}	$V_{CE}=V_{CEO}(max), V_{BE}=0$		0.4	mA
I _{EBO}	V _{EB} =5V, I _C =0		1.0	mA
*h _{FE}	I _C =0.3A, V _{CE} =4V	30		
	$I_{C}=3A, V_{CE}=4V$	15	75	
*V _{CE (sat)}	I _C =6A, I _B =0.6A		1.5	V
*V _{BE(on)}	$I_{C}=6A, V_{CE}=4V$		2.0	V
	<pre>*V_{CEO} I_{CEO} I_{CEO} I_{CES} I_{EBO} *h_{FE} *V_{CE (sat)}</pre>	$\begin{tabular}{ c c c c c c c } \hline & & & & & & & & & & & & & & & & & & $	$\begin{tabular}{ c c c c c c c c c c } & I_{C}=30mA, I_{B}=0 & & & & & & & & & & & & & & & & & & &$	$\begin{tabular}{ c c c c c c c c c c c } \hline & & & & & & & & & & & & & & & & & & $

*Pulse Test : Pulse width <300ms, Duty Cycle <2%

DYNAMIC CHARACTERISTIC

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Small Signal Current Gain	h _{fe}	I _C =0.5A, V _{CE} =10V, f=1KHz	20		
Transition Frequency	f _T	I _C =0.5A, V _{CE} =10V, f=1MHz	3		MHz

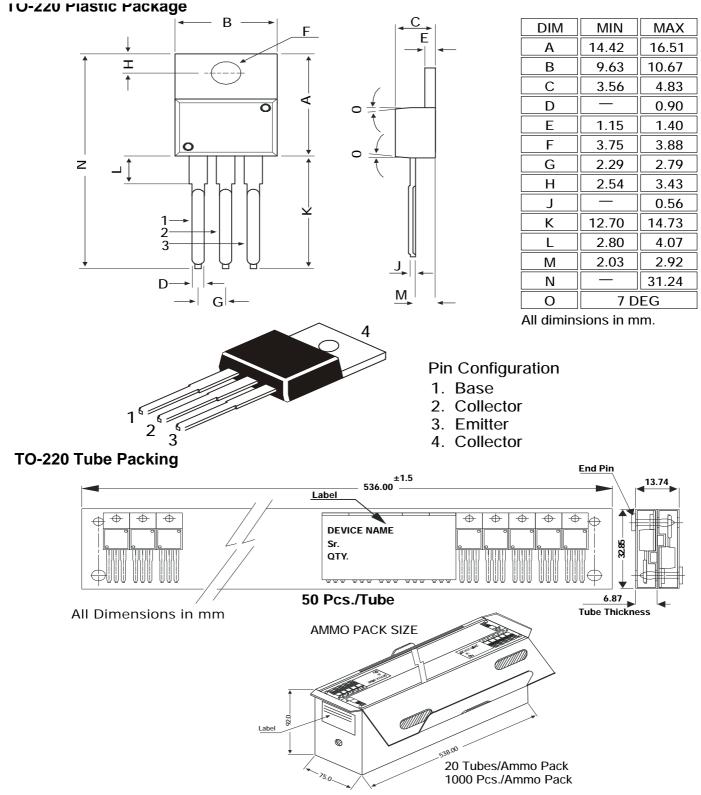
SWITCHING CHARACTERISTICS

DESCRIPTION	SYMBOL	TEST CONDITION	ТҮР	UNIT
Turn On Time	t _{on}	V_{cc} =30V, I_c =6A, I_{B1} = I_{B2} =0.6A,	0.6	μs
Turn Off Time	t _{off}	$R_L=5\Omega$	1.4	μs

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Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	GrWt
TO-220 /FP	200 pcs/polybag 50 pcs/tube	396 gm/200 pcs 120 gm/50 pcs	3" x 7.5" x 7.5" 3.5" x 3.7" x 21.5"	1.0K 1.0K	17" x 15" x 13.5" 19" x 19" x 19"	16.0K 10.0K	36 kgs 29 kgs

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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