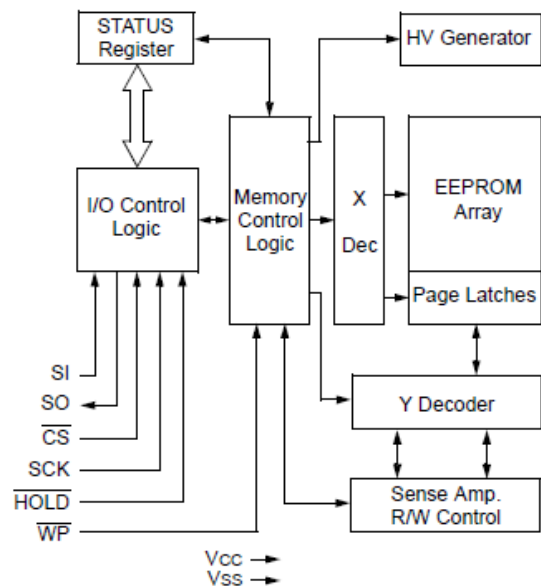**MICROCHIP**

25LC1024 1 Mbit SPI Bus Serial EEPROM

General Description:

The Microchip Technology Inc. 25LC1024 is a 1024 Kbit serial EEPROM memory with byte-level and page level serial EEPROM functions. It also features Page, Sector and Chip erase functions typically associated with Flash-based products. These functions are not required for byte or page write operations. The memory is accessed via a simple Serial Peripheral Interface (SPI) compatible serial bus. The bus signals required are a clock input (SCK) plus separate data in (SI) and data out (SO) lines. Access to the device is controlled by a Chip Select (CS) input.

Key Features:

- Single supply with operation from 1.7V to 5.5V
- Low-power CMOS technology
- 2-wire serial interface, I2C™ compatible
- Cascadable up to eight devices
- Schmitt Trigger inputs for noise suppression
- Output slope control to eliminate ground bounce
- 100 kHz and 400 kHz clock compatibility
- Page write time 5 ms maximum
- Self-timed erase/write cycle
- 16-byte page write buffer
- Hardware write-protect
- More than 1 million erase/write cycles
- Data retention >200 years

Applications:

- Energy Metering & Power Measurement
- Automotive
- Portable Instrumentation
- Medical and Power Monitoring

Related Products Information:

Mfr Part #	Farnell #	Newark #	Description
25LC1024-I/SN	1814899	73R8741	1 Mbit SPI Bus Serial EEPROM, SOIC
24AA025E48T-I/OT	1814900	73R8742	1 Mbit SPI Bus Serial EEPROM, TSSOP