Customer Information Notification

202302020I : Release of Errata Document Revision "6" for LS1012A

Issue Date: Mar 25, 2023 **Effective date:** Mar 26, 2023

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Management summary
Release of Errata Document Revision "6" for LS1012A Device.

Change Category

[ ]Wafer Fab Process	[ ]Assembly Process	[ ]Product Marking	[ ]Test Process	[ ]Design
[ ]Wafer Fab Materials	[ ]Assembly Materials	[ ]Mechanical Specification	[ ]Test Equipment	[X]Errata
[ ]Wafer Fab Location	[ ]Assembly Location	[ ]Packing/Shipping/Labeling	[ ]Test Location	[ ]Electrical spec./Test coverage
[ ]Firmware	[ ]Other			

PCN Overview
Description

The following changes have been made to the LS1012A Device Errata document:

The following errata were added:
• A-051238 - Hot plug-in/out on SATA Port Multiplier Ports (PMP) may not be detected
• A-050565 - A glitch on I/O pins during VDD core supply ramp up and ramp down

The following errata were revised:
• A-050428 - Speculative AT instruction using out-of-context translation regime could cause subsequent request to generate an incorrect translation
• A-010650 - I2C controller is unable to generate clocks when SDA is low
• A-009149 - USB 2.0 protocol data pins USB_D_M and USB_D_P do not support IEEE 1149.1
• A-007728 - The process of clearing the FTMx_SC[TOF] bit does not work as expected under a certain condition when the FTM counter reaches FTM_MOD value
• A-011218 - eDMA may not work with SPI
• A-009798 - USB high speed squelch threshold adjustment

Reason
Notify customer of the errata document release.

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

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