



Customer Information Notification

202302024I : Release of Errata Document Revision "2" for LS1028A/LS1027A Family of Devices

Note: This notice is NXP Company Proprietary.

Issue Date: Mar 26, 2023 **Effective date:** Mar 27, 2023

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Management summary

Release of Errata Document Revision "2" for LS1028A, LS1027A, LS1018A and LS1017A Family of Devices.

Change Category

| | | | | |
|--|--|--|--|---|
| <input type="checkbox"/> Wafer Fab Process | <input type="checkbox"/> Assembly Process | <input type="checkbox"/> Product Marking | <input type="checkbox"/> Test Process | <input type="checkbox"/> Design |
| <input type="checkbox"/> Wafer Fab Materials | <input type="checkbox"/> Assembly Materials | <input type="checkbox"/> Mechanical Specification | <input type="checkbox"/> Test Equipment | <input checked="" type="checkbox"/> Errata |
| <input type="checkbox"/> Wafer Fab Location | <input type="checkbox"/> Assembly Location | <input type="checkbox"/> Packing/Shipping/Labeling | <input type="checkbox"/> Test Location | <input type="checkbox"/> Electrical spec./Test coverage |
| <input type="checkbox"/> Firmware <input type="checkbox"/> Other | | | | |

PCN Overview

Description

The following changes have been made to the LS1028A, LS1027A, LS1018A and LS1017A Errata document:

The following errata were added:

- A-050089 - EMDIO (PF3) reads and writes may yield undesired results if performed simultaneously with per-port Physical Function (PF), Physical Station Interface (PSI), or Virtual Station Interface (VSI) register accesses.
- A-050757 - qDMA may hang on PCI Express outbound memory reads
- A-051238 - Hot plug-in/out on SATA Port Multiplier Ports (PMP) may not be detected
- A-051116 - Core soft resetting does not work
- A-050263 - SPI: Incorrect data may be transmitted in slave mode

The following errata were revised:

- A-011026 - Safe state is not removed from the channel output after fault condition ends if

SWOCTRL register is used to control the pin

- A-009149 - USB 2.0 protocol data pins USB_D_M and USB_D_P do not support IEEE 1149.1
- A-004985 - SerDes receiver high-speed data path is short in gain

Reason

Notify customer of the errata document release.

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

Contact and Support

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For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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