

Customer Information Notification

202302038I: MC56F80xxx Datasheet Update to Rev 2.1

Note: This notice is NXP Company Proprietary.

Issue Date: Mar 22, 2023 Effective date: Mar 23, 2023

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Management summary

The MC56F80xxx Datasheet have been updated.

Change Category

[]Wafer Fab Process	[]Assembly Process	[]Product Marking	[]Test Process	[]Design
[]Wafer Fab Materials	[]Assembly Materials	[]Mechanical Specification	[]Test Equipment	[]Errata
[]Wafer Fab Location	[]Assembly Location	[]Packing/Shipping/Labeling	[]Test Location	[]Electrica spec./Test coverage
[]Firmware	[X]Other: Da	atasheet		

PCN Overview

Description

NXP Semiconductors announces that MC56F80xxx Datasheet have been updated to new revisions: MC56F80xxx Rev2.1

MC56F80xxx, Rev2.1 summary of Changes:

1. enable QSPI function for MC56F806XX sub-family

The updated MC56F80xxx Datasheet can be found at:

https://www.nxp.com/products/processors-and-microcontrollers/additional-mpu-mcus-architectures/digital-signal-controllers/32-bit-56800ex-ef-core/dscs-with-integrated-fpu-and-trigonometric-math-engine-with-opamp-and-quadrature-decoder:MC56F80xxx

Reason

MC56F80XXX Datasheet enable QSPI function for MC56F806XX sub-family

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Data Sheet Revision

A new datasheet will be issued

Additional information

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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