

Customer Information Notification

202306011I : MC9S12ZVC Reference Manual Update from Rev2.3 to Rev2.4

Note: This notice is NXP Company

Proprietary.

Issue Date: Jun 28, 2023 Effective date: Jun 29, 2023

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Change Category

[]Wafer Fab Process	[]Assembly Process	[]Product Marking	[]Test Process	[]Design
[]Wafer Fab Materials	[]Assembly Materials	[]Mechanical Specification	[]Test Equipment	[]Errata
[]Wafer Fab Location	[]Assembly Location	[]Packing/Shipping/Labeling	[]Test Location	[]Electrica spec./Test coverage
	0.4204			

[]Firmware [X]Other: Reference Manual Update

PCN Overview

Description

NXP Semiconductors announces a Reference Manual update for the MC9S12ZVC from Rev 2.3 to Rev 2.4.

The revision history (Appendix) included in the updated document provides a detailed description of the changes.

The MC9S12ZVC Reference Manual is attached with this notification and can be found at: https://www.nxp.com/products/processors-and-microcontrollers/additional-mpu-mcus-architectures/s12-magniv-mixed-signal-mcus/s12zvc-mixed-signal-mcu-for-automotive-and-industrial-can-applications:S12ZVC

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02.

Reason

The Reference Manual has been updated to provide additional technical clarification.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: view online

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

Name NXP Tech Support

Position NXP Technical Support

e-mail

address <u>tech.support@nxp.com</u>

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NXP Quality Management Team.

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