



## Customer Information Notification

202401019I : S32K116/ K118/ K142/ K144/ K146/ K148 Errata Update

**Note:** This notice is NXP Company Proprietary.

**Issue Date:** Feb 08, 2024 **Effective date:**Feb 09, 2024

Here is your personalized notification about a NXP general announcement.  
For detailed information we invite you to [view this notification online](#)

### Change Category

<input type="checkbox"/> Wafer Fab Process	<input type="checkbox"/> Assembly Process	<input type="checkbox"/> Product Marking	<input type="checkbox"/> Test Process	<input type="checkbox"/> Design
<input type="checkbox"/> Wafer Fab Materials	<input type="checkbox"/> Assembly Materials	<input type="checkbox"/> Mechanical Specification	<input type="checkbox"/> Test Equipment	<input checked="" type="checkbox"/> Errata
<input type="checkbox"/> Wafer Fab Location	<input type="checkbox"/> Assembly Location	<input type="checkbox"/> Packing/Shipping/Labeling	<input type="checkbox"/> Test Location	<input type="checkbox"/> Electrical spec./Test coverage
<input type="checkbox"/> Firmware	<input type="checkbox"/> Other			

## PCN Overview

### Description

NXP Semiconductors announces errata update for the following products associated with this notification:

1. S32K116 mask set 0N96V from revision 27/JUL/2023 to revision 17/JAN/2024.
2. S32K118 mask set 0N97V from revision 27/JUL/2023 to revision 15/JAN/2024.
3. S32K142 mask set 0N33V from revision 27/JUL/2023 to revision 15/JAN/2024.
4. S32K144 mask set 0N57U from revision 27/JUL/2023 to revision 15/JAN/2024.
5. S32K146 mask set 0N73V from revision 27/JUL/2023 to revision 15/JAN/2024.
6. S32K148 mask set 0N20V from revision 27/JUL/2023 to revision 15/JAN/2024.

The revision history included in the updated document provides a detailed description of the change. Please see document revision history for more information.

The updated errata documents referenced above are attached to this notification and can found at below link:

<https://www.nxp.com/products/processors-and-microcontrollers/s32-automotive-platform/s32k-auto-general-purpose-mcus/s32k1-microcontrollers-for-automotive-general-purpose:S32K1>

Corresponding ZVEI Delta Qualification Matrix ID: SEM-DS-02.

### Reason

The errata will be published to provide additional technical clarification on some device features.

## Identification of Affected Products

Product identification does not change

## Anticipated Impact on Form, Fit, Function, Reliability or Quality

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No Impact on form, fit, function, reliability or quality

No changes were made to the current production device. The errata describe existing conditions identified on current production devices.

## Additional information

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Additional documents: [view online](#)

## Contact and Support

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For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

<b>Name</b>	NXP Tech Support
<b>Position</b>	NXP Technical Support
<b>e-mail address</b>	<a href="mailto:tech.support@nxp.com">tech.support@nxp.com</a>

At NXP Semiconductors we are constantly striving to improve our product and processes to ensure they reach the highest possible Quality Standards. Customer Focus, Passion to Win.

NXP Quality Management Team.

### About NXP Semiconductors

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