



Customer Information Notification

202502014I : S32G2 and S32G3 Wafer Bump Site Expansion from TSMC_AP2 to ASE-KH

Note: This notice is NXP Company Proprietary.

Issue Date: Mar 08, 2025 **Effective Date:** Apr 08, 2025

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For detailed information we invite you to [view this notification online](#)

Change Category

- | | | | | |
|--|--|--|--|---|
| ☐ Wafer
Fab
Process | ☐ Assembly
Process | ☐ Product Marking | ☐ Test
Process | ☐ Design |
| ☐ Wafer
Fab
Materials | ☐ Assembly
Materials | ☐ Mechanical Specification | ☐ Test
Equipment | ☐ Errata |
| ☐ Wafer
Fab
Location | ☐ Assembly
Location | ☐ Packing/Shipping/Labeling | ☐ Test
Location | ☐ Electrical
spec./Test
coverage |
- ☐ Firmware ☒ Other: Wafer Bump Location

Notification Overview

Description

NXP Semiconductors announces the Wafer Bump site expansion for the S32G2 (Mask set 1P77B) and S32G3 (Mask set 1P72B) from current TSMC_AP2, Taiwan to ASE-Kaohsiung (ASE-KH), Taiwan wafer bump site.

Wafer bump site expansion was successfully qualified adhering to NXP specifications.

Please see the attached file(s) for additional details.

Reason

Qualification of ASE-KH Wafer Bump Site is required for manufacturing flexibility and customer supply assurance.

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

No Impact on form, fit, function, reliability or quality

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

For specific questions on this notice or the products affected please contact our specialist directly:

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NXP Quality Management Team.

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